Subthreshold behavior of dual-bit nonvolatile memories with very small regions of trapped charge

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Abstract—We propose an analytical approach to investigate the electrostatic impact of very small charged regions in the gate dielectric of dual-bit nonvolatile memory cells based on discrete trapping sites, such as SONOS, NROM, or nanocrystal memories. Our model is based on the analytical solution of the Poisson equation for the surface potential in a fresh memory cell in subthreshold conditions. Then, we evaluate the effect of a small pocket of trapped charge on the surface potential using the superposition principle. Our proposed model is particularly accurate for small charged regions, down to the effective charged length $L_2 \approx 10$ nm and for charge density up to $Q \approx 10^{13}$ cm$^{-2}$. In addition, the proposed model represents a complementary approach to a previously developed model which was suitable for larger charged regions. Relevant consequences of the asymmetric charging of the storage layer on the electrical characteristics of discrete-trap memories are thoroughly analyzed. An analytical expression for the subthreshold slope factor $S$, the threshold voltage $V_{th}$, and a method for extracting an “effective” distribution of charges from the transfer characteristics are derived.

Index Terms—Device modeling, discrete-trap memory, dual-bit, nanocrystal memory, nonvolatile memory, NROM, reverse read, second bit effect, SONOS.

I. INTRODUCTION

Nonuniform injection in discrete-trap memories has recently attracted much attention in the literature for the possibility of encoding more than one bit for each memory cell. During the channel hot electron (CHE) stress, for instance, electrons are injected near the drain in the trapping medium, which can consist of an amorphous layer, as in NROM [2] and SONOS [3], or of a nanocrystal layer, as in nanocrystal flash memories [4], [5].

Nonuniform charging typically poses two issues: a difference between the threshold voltages extracted with forward ($V_{ds} > 0$) or reverse ($V_{ds} < 0$) read and a subthreshold slope degradation observed in reverse read [2], [4], [5].

Published studies on this subject are based on experiments or on two-dimensional (2-D) numerical simulations, in which much attention is devoted to the surface potential $\Psi_S$ [6], [7]. One analytical approach, based on charge sharing, is able to provide an analytical approximated formula for the subthreshold slope $S$ [8], and another analytical approach, based on the gradual channel approximation, is able to model the dual-bit performance in the triode operation region [9]. We have previously proposed an approach [1] based on the analytical calculation of the surface potential profile along the channel of a memory cell ideally consisting of a series of two MOSFETs, which is able to provide a reasonable solution in the limit of charged region larger than 20 nm.

The model we propose here is able to both extend the area of analysis to smaller regions and to provide an analytical formula for the subthreshold slope and the threshold voltage based on the surface potential approach. It starts from an analytical expression of the surface potential [1], [11] along the channel of a fresh cell and then it adds the effect of a charged region in the trapping medium close to the drain, via the superposition principle, provided that the memory cell is in a subthreshold region (where mobile charge is negligible with respect to space charge).

The main results of this new model are validated through a comparison with detailed numerical simulations performed with a 2-D TCAD package (ATLAS from SILVACO [10]).

II. ANALYTICAL MODEL FOR BULK MEMORY

A. Fresh Cell

The surface potential behavior along the channel can be computed as described in [11] and extended in [1] so that here we report only the results. The analytical solution to $\Psi_S$ is a linear combination of hyperbolic sines

$$
\Psi_S(y) = (\Psi_T - \Psi_L) \frac{\sinh(y/\lambda)}{\sinh(L/\lambda)} + (\Psi_I - \Psi_L) \frac{\sinh[(L - y)/\lambda]}{\sinh(L/\lambda)} + \Psi_L \quad (1)
$$

where $y = 0 (y = L)$ corresponds to the source (drain) contact, $\Psi_T = V_{th} + V_a - V_b$, and $\Psi_I = V_{th} + V_d - V_b$, where $V_{th}$ is the built-in voltage at the drain-bulk and source-bulk junctions, $V_a$ is the source voltage, $V_d$ is the drain voltage, $V_b$ is the bulk voltage, and $\Psi_L$ is the long channel surface potential [12]. The potential is set to zero in the bulk.
The parameter $\lambda$ is defined as

$$\lambda \equiv \sqrt{\frac{\varepsilon_{sid} X_{dcP}}{\varepsilon_{ox}}}$$

(2)

where $t_{ox}$ is the equivalent oxide thickness, $X_{dcP}$ is the space charge region thickness, and $\varepsilon_{sid}$ is the silicon (oxide) permittivity. Clearly, $\lambda$ is a crucial parameter to describe short channel effects (SCEs), as it describes the influence of the $p$-$n$ junctions that are close to the drain terminal on the behavior of $V_s$. The higher the value of $\lambda$, the smoother the $V_s$ profile appears, and the weaker is the coupling between gate and channel. We stress that in this characteristic length we find the crucial parameters impacting on dual-bit performance. Moreover, $\lambda$ is a function of $\eta$, which we obtain by fitting the surface potential behavior obtained from the numerical simulation with that obtained from the analytical model. Let us stress the fact that $\eta$ is evaluated only once, for the fresh cell, and is valid for all charged configurations of the memory cell.

**B. Electrostatic Effects of Trapped Charge**

As a second step, the electrostatic effect of charges is considered via the superposition principle. As in [1], we assumed a uniform trapped charge region which is localized in the trapping layer next to the drain junction, while the region over the rest of the channel is free of charges. We believe that the simplified step-function distribution allows us to understand the programming behavior in terms of two effective parameters, an effective charged length $L_2$ and an effective charged density $Q$. When the overall channel is under weak inversion, the amount of mobile charge is so small that the Poisson equation can be considered linear. As shown in Fig. 1, in the 2-D structure the perturbative potential $\Psi_{SP}$, due to a rectangular charged region of height $t_{tm}$ and length $L_2$, is obtained by integrating the elementary contributions along the vertical direction $x$ and longitudinal direction $y$ [13]

$$\Psi_{SP}(y) = \frac{-\rho}{\pi(\varepsilon_{sid} + \varepsilon_{ox})} \left[ \int_{L_1+L_2}^{L_1} dy' \int_{y_1}^{y_2} \ln \left( \sqrt{(y-y')^2 + x^2} \right) dx \right]$$

(3)

where $\rho$ is the density of injected charge per unit volume and the apex $A$ refers to the integration to the actual point of charge, $t_1$ is the bottom oxide thickness and $L_1 = L - L_2$. Equation (3) has a closed-form solution. The boundary conditions of the Poisson equation are simplified as shown in Fig. 1 and can be taken into account by a proper configuration of image charges. The gate contact and the drain junction can be approximated as two perfect metals (one horizontal and one vertical plate, respectively), which enforce Dirichlet boundary conditions; therefore, the “real” charges must be suitably mirrored as indicated in Fig. 1. The Si/SiO$_2$ interface causes a change of permittivity from $\varepsilon_{sid}$ to $\varepsilon_{ox}$ and this effect can be reproduced with basic electrostatic considerations [13], multiplying all the charges by a factor $\alpha = 2\varepsilon_{sid}/(\varepsilon_{sid} + \varepsilon_{ox})$.

Each of the four pockets of charge shown in Fig. 1 provides a potential given by an integral equivalent to (3) with a closed-form solution. By summing up all four contributions, we obtain the perturbative potential $\Psi_{SP}$ and finally the surface potential $\Psi_{Stot} = \Psi_S + \Psi_{SP}$.

The current is calculated afterward as a diffusion dominated process [14], depending on the minimum of the surface potential $\Psi_{Smin} = \min_y \{ \Psi_{Stot} \}$

$$I_{ds} = \frac{\mu_{eff} W}{L_i} \sqrt{\frac{\varepsilon_{sid} Q_n}{2(\Psi_{Smin} + V_b)}} \left( \frac{KT}{q} \right)^{\frac{1}{2}} n_i^2 \frac{1}{N_{sub}} \exp \left[ \frac{q(\Psi_{Smin} + V_b)}{KT} \right]$$

(4)

where $\mu_{eff}$ is the effective mobility of electrons in the channel, $Q_{sub}$ is the doping level of the bulk, $T$ is the absolute temperature, $q$ is the electron charge, $n_i$ is the intrinsic electron concentration, and $W$ is the cell width. The choice of $L_i (i = 1,2)$ depends on the region that includes the minimum of the surface potential $\Psi_{Smin}$. If there is a complete punchthrough, $\Psi_{Smin}$ stands under the uncharged region; thus, $L_i = L$. We stress that we numerically verify that (4) describes well the drain current in subthreshold region with reasonable error, for any value of $L_2$; moreover, due to the fact that the dual-bit behavior is usually sensed at high $V_{ds}$ (i.e., $V_{ds} \approx 1.5$ V), we neglected in (4) the effect of an applied drain voltage.

**C. Domain of Validity of Analytical Model**

We will perform the analysis for very low $V_D$, when the channel is in subthreshold or weakly inverted, where the linearity of the Poisson problem is retained. Approaching the inversion condition, such an assumption does not hold. However, provided that in the subthreshold region the drain current is exponential, the analytical transfer characteristic is obtained by extrapolating the exponential fit at low $V_D$ (we extract the threshold voltage for $I_{ds} = 10$ nA).

For a very high density of charge ($Q \approx 10^{13}$ cm$^{-2}$) and a very short charged region ($L_2 \approx 7$ nm), the error between
the analytical model predictions and the TCAD results becomes large (\(>15\%\)). In such conditions, the model is no more applicable because of the occurrence of a punchthrough effect. From TCAD simulations, we have noticed that the maximum of the potential is far from the surface and the current flows at a certain distance from the interface; therefore, the model underestimates the drain current.

III. ANALYTICAL EXPRESSION OF SUBTHRESHOLD SLOPE FACTOR \(S\) AND THRESHOLD VOLTAGE \(V_{Th}\)

Our analytical approach to the dual-bit behavior, in discrete trap memories, allows us to provide an analytical expression for the subthreshold slope factor \(S\) and the threshold voltage. Concerning \(S\), the following reasoning is applied to the reverse reading, as the subthreshold slope degradation is visible for small values of \(L_2\) and high \(Q\) only in reverse reading, while the forward reading shows the same characteristic of the fresh cell. The definition of the subthreshold slope factor \(S\) \([14]\) is

\[
S = \frac{dV_g}{d(\log I_{ds})} = \frac{dV_g}{dV_{Smin}} \frac{dV_{Smin}}{d\log I_{ds}}.
\]  

(5)

We should properly evaluate the factor \(dV_g/(dV_{Smin})\), and, to this aim, we need first to find the \(y_0\) coordinate for which \(dV_{Stot}(y)/dy = 0\). It is clear that in this way the derivation does not provide an explicit solution since the expression of \(V_{Stot}(y)\) is not simple \([\text{see (1) and (3)}]\). A simpler way to obtain an approximated surface potential \(V_{S\text{Stot}}\) is to perform a Taylor expansion of \(V_{Stot}\) around the middle of the charged region (\(y = L_1 + L_2/2\)) close to the likely minimum of the function

\[
\Psi_{S\text{Stot}}(y) = \Psi_{S\text{Stot}}|_{y = \bar{y}} + \frac{d\Psi_{S\text{Stot}}}{dy}|_{y = \bar{y}} \cdot (y - \bar{y}) + \frac{1}{2} \frac{d^2\Psi_{S\text{Stot}}}{dy^2}|_{y = \bar{y}} \cdot (y - \bar{y})^2.
\]  

(6)

Putting to zero the derivative of \(\Psi_{S\text{Stot}}\) with respect to \(y\), we find the minimum \(y_0\) and the minimum of the surface potential \(V_{S\text{min}}\)

\[
y_0 = -\frac{d\Psi_{S\text{Stot}}}{dy}|_{y = \bar{y}} + \bar{y}, \quad V_{S\text{min}} = \Psi_{S\text{Stot}}(y_0).
\]  

(7)

Since \(V_{Sp}\) does not depend on \(V_g\), in the end, we can write the following analytical formula:

\[
\frac{dV_{Sp}}{dV_g} = \frac{dV_S}{dV_g} + \frac{d^2V_S}{dy^2} \cdot \Delta \bar{y}
\]  

(8)

where \(\Delta \bar{y} \equiv y_0 - \bar{y}\) and all terms on the right-hand side are calculated in \(y = \bar{y}\) and \(V_{\bar{g}} = V_{Fb} + \phi_F + \gamma \sqrt{2\phi_F}\), i.e., weak inversion \((V_{Fb}\) is the flat band voltage, \(\gamma\) the body factor, and \(\phi_F\) the bulk Fermi level). A third-order term in \((8)\) would only slightly improve the overall precision and therefore is neglected.

The terms of \((8)\) are

\[
\frac{dV_S}{dV_g} = \left[ 1 - \frac{\sinh \left( \frac{\bar{y}}{\lambda} \right) + \sinh \left( \frac{L - \bar{y}}{\lambda} \right)}{\sinh \left( \frac{\bar{y}}{\lambda} \right)} \right] \times \left[ 1 - \frac{1}{\sqrt{\gamma^2 + 4(2\phi_F + \gamma \sqrt{2\phi_F})}} \right]^{-1}
\]

\[
\frac{d^2V_S}{dy^2} = \frac{\cosh \left( \frac{L - \bar{y}}{\lambda} \right) - \cosh \left( \frac{\bar{y}}{\lambda} \right)}{\lambda \sinh \left( \frac{\bar{y}}{\lambda} \right)} \times \left[ 1 - \frac{1}{\sqrt{\gamma^2 + 4(2\phi_F + \gamma \sqrt{2\phi_F})}} \right]^{-1}
\]

\[
\frac{d^2V_{Sp}}{dy^2} = \frac{\rho q}{\pi \epsilon \epsilon_0} \left[ \bar{y} \left[ \frac{t_{tm}(L_1 - \bar{y})}{(L_1 - \bar{y})^2 + R \cdot M} \right] + \frac{M}{2} \arctan \left( \frac{t_{tm}(L_1 - \bar{y})}{(L_1 - \bar{y})^2 + R \cdot M} \right) \right]
\]

\[
- \arctan \left[ \frac{(L - \bar{y})t_{tm}}{(L - \bar{y})^2 + R \cdot M} \right] - \arctan \left[ \frac{(L_1 - \bar{y})t_{tm}}{(L_1 - \bar{y})^2 + R \cdot M} \right]
\]

\[
- \arctan \left[ \frac{(L_1 + 2L_2 - \bar{y})t_{tm}}{(L_1 + 2L_2 - \bar{y})^2 + R \cdot M} \right],
\]  

(9)
The threshold voltage $V_{th}$ is the $V_g$ corresponding to a given $I_{ds}$. We have to analytically find the behavior of $V_g$ as a function of $\Psi_{S\text{\tiny min}}^*$, which comes from the inversion of (6). Stressing the fact that in (6) the minimum $y_0$ is only slightly dependent on $V_g$, we consider it constant at the value of $V_g \equiv V_{th} - L_t = V_V + 2\phi_F + \gamma \sqrt{2\phi_F}$ (i.e., at the onset of inversion). Then, the only dependence on $V_g$ in (1), (6), and (7) resides in the long channel surface potential, developed around $\Psi_L = 2\phi_F$.

$$\Psi_L = \frac{V_g - V_{th-L_t}}{1 + \frac{\gamma}{\sqrt{2\phi_F}}} + 2\phi_F.$$  

(10)

By inverting (6), we therefore obtain

$$V_{th} = \left( -\frac{\Psi_{S\text{\tiny min}}^*(I_{ds}) - K}{H} - 2\phi_F \right) \left( 1 + \frac{\gamma}{\sqrt{2\phi_F}} \right) + V_{th-L_t}$$

(11)

where

$$K = \left[ \psi_t \frac{\sinh(\tilde{y}/\lambda)}{\sinh(L/\lambda)} + \psi_r \frac{\sinh(\tilde{L}/\lambda)}{\sinh(L/\lambda)} \right]$$

$$\times \left( 1 + \frac{\Delta \tilde{y}}{\lambda} \right) + \Psi_S \frac{d\Psi_S}{d\tilde{y}} \Delta \tilde{y}$$

$$H = \frac{\sinh(\tilde{y}/\lambda)}{\sinh(L/\lambda)} + \frac{\sinh(\tilde{L}/\lambda)}{\sinh(L/\lambda)} - 1$$

$$+ \frac{\Delta \tilde{y}}{\lambda} \left[ \cosh(\tilde{y}/\lambda) - \cosh(\tilde{L}/\lambda) \right]$$

where in $K$, the $\Psi_S$ contributions obviously exist only if we calculate the threshold voltage of a charged cell. $\Psi_{S\text{\tiny min}}^*(I_{ds})$ comes from the inversion of (4) and depends on the drain current at which the threshold voltage is calculated.

In the development of (8)–(11), we assumed that the minimum $y_0$ (7) resides under the charged region $L_t$. If this region is so small that $y_0$ resides under $L_t$, the Taylor expansion of $\Psi_{S\text{\tiny tot}}$ should be made around the middle of the cell, and the previously mentioned formulas are still valid with $\tilde{y} = L/2$. Indeed, we note that in Fig. 2, if $L_t \lesssim 15$ nm, the subthreshold slope regains the fresh cell solution and the threshold voltage shift becomes zero.

IV. NUMERICAL AND ANALYTICAL RESULTS

In order to assess the viability of this model, the 2-D numerical TCAD tool ATLAS has been used [10]. The device under investigation is an idealized typical NROM device. The width and length of the cell are 0.16 and 0.28 $\mu$m, respectively, drain and source n-doping is at $N_d = 10^{19}$ cm$^{-3}$, the p-channel doping is $N_{S\text{\tiny th}} = 5 \times 10^{17}$ cm$^{-3}$, the bottom oxide thickness is $t_1 = 7$ nm, and the top oxide thickness is $t_2 = 9$ nm. Concerning the trapping layer, for the sake of simplicity, we have considered an SiO$_2$ layer $t_{tr} = 3.6$ nm, equivalent, from an electrostatic point of view, to a layer of silicon nitride of 7 nm. We have verified that even in the case of nonuniform charging, the substitution of the silicon nitride layer with an equivalent silicon oxide layer has a negligible effect on the results of the simulation. In the following discussion, the threshold voltage is taken at $I_{ds} = 10$ nA.

In Fig. 2, we show the comparison between TCAD and analytical results, concerning the subthreshold slope factor $S$ and the total threshold voltage shift $\Delta V_{th-tot}$, for different values of $L_t$ and $Q$. In Fig. 2(a), we note that for very small values of $L_t$, $S$ is close to that of a fresh cell, while by raising the value of $L_t$ the subthreshold slope degrades up to a maximum value ($\sim L_t \sim 30$ nm); then, $S$ goes again to the smaller fresh cell limit as the portion of the charged cell approaches the overall length. On the other hand, in Fig. 2(b) we note that, by raising the value of $Q$, the subthreshold slope keeps degrading. A similar behavior of the subthreshold slope factor has been previously shown in [8].

V. CONTOUR PLOT OF $\Delta V_{th-tot}$ AND $S$ WITH RESPECT TO $L_t$ AND $Q$

In the previous section, the behavior of the forward read transfer characteristics has not been mentioned. Such an issue has been addressed in our previous approach [1], and, given that the present model is tailored specifically for very short charged length $L_t$, the forward read would provide an $I_D - V_{GS}$ curve basically identical to that of a fresh cell. If we consider a large
Fig. 3. Contour plots of $\Delta V_{\text{th-tot}}$ (dashed line) and $S$ (solid line) as function of $L_2$ and $Q$ for $|V_{\text{DS}}| = 1.5$ V. From experimental data, we find cross point between $\Delta V_{\text{th-tot}}$ and $S$ values, and we are able to extract information on charge distribution injected in trapping medium through $L_2$ and $Q$. Two sample crosses for $I_D = V_{GS}$ are shown in Fig. 4.

$L_2$ ($L_2 \approx 70$ nm), the forward read follows the behavior shown in [1], but, in that range of $L_2$, the subthreshold slope (forward or reverse read) is similar to that of a fresh cell as shown already in Fig. 2.

With the help of this model, we can concentrate, indeed, on the behavior of the subthreshold slope and on the behavior of the total threshold voltage shift, focusing on the transfer characteristics of the fresh cell and of the written cell in the reverse read.

In Fig. 3, we provide two contour plots of $\Delta V_{\text{th-tot}}$ and $S$, with respect to $L_2$ and $Q$. From the experiments, we have information on the threshold voltage in the fresh read ($V_{\text{th-fresh}}$), reverse read ($V_{\text{th-R}}$), and the value of the subthreshold slope factor $S$. We identify one cross point on the map of Fig. 3 and we deduce the features of the effective charge distribution trapped near the drain, $L_2$ and $Q$.

We stress the fact that we can use this model to get other contour plots of the programming windows, not only of $\Delta V_{\text{th-tot}}$ and $S$, depending of the information that we have at hand from experiments, and extract the two features of the pocket of charge.

These kinds of contour plots are interesting in the sense that they provide a simple information on the actual state of the pocket of injected charge. For instance, they can be used also during data retention experiments to understand whether the diffusion process among traps, i.e., $Q \times L_2 = \text{const.}$, or the charge leakage towards the channel, i.e., $L_2 = \text{const.}$, is the predominant effect affecting data retention: a clear different electrical behavior would be visible in these contour plots function of $Q$ and $L_2$ [15].

Just as a check of the validity of such a map, we compare results from our model with numerical simulations. We extract from the map $Q$ and $L_2$, for a pair of $\Delta V_{\text{th-tot}}$ and $S$, and use them in the 2-D TCAD simulator to see if simulated electrical characteristics are in agreement with the given values of $\Delta V_{\text{th-tot}}$ and $S$. The results of such a comparison for two typical cases are shown in Fig. 4 where, according to the contour plots, $\Delta V_{\text{th-tot}} = 1.62$ V in both cases and $S = 173$ mV/dec or $S = 236$ mV/dec; the reasonable agreement obtained shows that the proposed maps are useful to extract an “effective” distribution of the trapped charge.

VI. CONCLUSION

We have presented an analytical model that provides a simple electrostatic view of dual-bit memory electrical behavior. The reverse mode read and forward mode read of a programmed cell with nonuniform distribution of injected charge are analytically computed. The main issues such as the higher threshold voltage in reverse read than in forward read and the subthreshold slope degradation in reverse read are all successfully addressed. In particular, the subthreshold slope degradation is caused by a very small pocket of charge ($L_2 \approx 30$ nm) with very high density of charge ($Q \approx 10^{12}$ cm$^{-2}$). The overall reasoning needs only one physically based $\eta$ parameter, which depends on the features of the fresh cell and is completely independent of the effective distribution of the pocket of charge. The proposed model pushes the limits of the analysis to a distribution of charges with $L_2 \approx 10$ nm and $Q \approx 10^{13}$ cm$^{-2}$.

With the help of this model we are able to extract the features of the injected pocket of charge from simple contour plots as provided in Fig. 3. Moreover, an analytical approximated formula for the subthreshold slope factor and the threshold voltage are provided. We consider this model an effective tool for the comprehension of dual-bit memory behavior of nonvolatile memories such as SONOS, NROM, or nanocrystal.

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Gérard Ghibaudo was born in France, in 1954. He graduated, in 1979, received the Ph.D. degree in electronics, in 1981, and the State Thesis degree in physics, in 1984, all from the Polytechnics Institute of Grenoble, Grenoble, France.

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